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end

between the host and the display, and also for allowing the independent electronic circuit to have a direct access to the On Screen Display capability of the display. The service channel may be advantageously a DDC/CI communication link, and the electronic circuit may be a hardware monitoring circuit which is operational even before the booting process of the processor. No additional Liquid Crystal Display is thus required.

IN THE CLAIMS

Please amend claims 1-6, 8-11, 13 and 15-16 as shown. Clean copies of claims 1-20 are presented on the following pages 3-9 of this Amendment. The marked-up version of claims 1-6, 8-11, 13 and 15-16 can be found in Appendix A.

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Cont.

1. (Once Amended) Communication process between an Information Handling System (IHS) and at least one display having On Screen Display (OSD) capability; wherein

said IHS system includes a processor under control of an operating system, a graphics system and an electronic circuit operating independently of said processor and said graphics system,

said at least one display receives a graphics channel comprising the graphics signals generated by said graphics system and a service channel allowing interaction between said at least one display and said operating system;

the process being characterized in that said service channel is also used to permit said independent electronic circuit to have access to the On Screen Display (OSD) capability of said at least one display in order to display text and/or graphics independently of said processor and said operating system.

2. (Once Amended) Process according to claim 1 characterized in that said service channel of said display consists of a bi-directional serial communication link which is compatible with the I²C protocol, providing either DDC or DDC/CI communication support with said operating system, as well as an I²C communication link between said independent electronic circuit and said at least one display in order to provide to said electronic circuit a direct access to the OSD capability of said at least one display.

3. (Once Amended) Process according to claim 2 characterized in that said electronic circuit consists of a hardware monitoring circuit displaying monitoring feedback information to the user via said service channel.

4. (Once Amended) Process according to claim 1 characterized in that said graphics system is either an AGP or PCI graphics card which is plugged into a corresponding AGP or PCI slot having at least two conductors being reserved for said I²C communication link conveying the OSD commands to be directed to said at least one display.

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5. (Once Amended) Process according to claim 3 characterized in that said hardware monitoring circuit is connected via a network to said IHS system in order to provide an alarm on Local Area Network (LAN) capability.
6. (Once Amended) Information Handling System (IHS) comprising:
a processor arranged to operate under the control of an operating system, a graphics system and an electronic circuit operable independently of said processor;
at least one display having On Screen Display (OSD) capability and including first receiving means for receiving a graphics channel upon which graphics signals generated by said graphics system are transmitted, and second receiving means for receiving a service channel for allowing interaction between said at least one display and said operating system;
characterized in that said service channel and said display are arranged to permit said independent electronic circuit to access the On Screen Display (OSD) capability of said at least one display in order to display text and/or graphics independently of said processor and said operating system.
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7. Information Handling System according to claim 6 characterized in that said service channel consists of a bi-directional serial communication link.
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8. (Once Amended) Information handling system as claimed in claim 7 wherein said serial communication link is compatible with the I²C protocol, and provides a DDC or a DDC/CI communication interface with said processor as well as a I²C communication link between said independent electronic circuit and said at least one display in order to provide to said electronic circuit a direct access to the OSD capability of said at least one display.
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9. (Twice Amended) Information handling system according to claim 7 characterized in that said electronic circuit is a hardware monitoring circuit for displaying monitoring feedback information to the user via said service channel.
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10. (Once Amended) Information Handling System according to claim 8 characterized in that said graphics systems includes:

a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input for receiving a second I²C communication channel provided by said hardware monitoring circuit;

B5 said arbitration means providing arbitration between said first and said second I²C communication links so that the hardware monitoring circuit and the processor can both get access to said second receiving means of said at least one display.

11. (Once Amended) Information Handling System according to claim 10 characterized in that said graphics systems is an AGP or PCI graphics card which is plugged into a corresponding AGP or PCI graphics slot having at least two wires which are dedicated for the communication of said second I²C protocol communication channel.

12. Information Handling System according to claim 11 characterized in that said graphics system and said processor are located on the same motherboard.

B6 13. (Twice Amended) Graphics system for use in an Information Handling System (IHS) as defined in claim 6 characterized by:

a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input receiving a second I²C communication channel provided by said hardware monitoring circuit;

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said arbitration means for providing arbitration between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.

14. Graphics system according to claim 13 characterized in that said arbitration means are arranged to prevent the access of said service channel to one among said first and second I²C communication links until a preceding I²C transaction has been successfully completed.

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15. (Once Amended) Display device having On Screen Display (OSD) capability for use in an Information Handling System (IHS) including a processor under control of an operating system, a graphics system and an electronic circuit operating independently of said processor and said graphics system ,

said display having one or more connectors for receiving a graphics channel comprising graphics signals generated by said graphics system and a service channel allowing interaction between said display and said operating system;

characterized by means responsive to commands in said service channel for controlling the On Screen Display capability independently of the operation of the processor and the operating system.

16. (Once Amended) Display device according to claim 15 characterized in that said service channel consists of a bi-directional serial communication link which is compatible with the I²C protocol, and providing either DDC or DDC/CI communication with said operating system, as well as an I²C communication link between said independent electronic circuit and said display in order to provide to said electronic circuit a direct access to the OSD capability of said display.

17. Graphics system for use in an Information Handling System (IHS) as defined in claim 7 characterized by:

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a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input receiving a second I²C communication channel provided by said hardware monitoring circuit;

said arbitration means for providing between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.

18. Graphics system for use in an Information Handling System (IHS) as defined in claim 8 characterized by:

a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input receiving a second I²C communication channel provided by said hardware monitoring circuit;

said arbitration means for providing between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.

19. Graphics system for use in an Information Handling System (IHS) as defined in claim 9 characterized by:

a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second

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input receiving a second I²C communication channel provided by said hardware monitoring circuit;

said arbitration means for providing between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.

20. Graphics system for use in an Information Handling System (IHS) as defined in claim 10 characterized by:

a graphics engine for providing graphics signals to said graphics channel and a first I²C communication channel complying with the DDC/CI protocol for controlling said at least one display;

arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input receiving a second I²C communication channel provided by said hardware monitoring circuit;

said arbitration means for providing between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.